IN THE SPECIFICATION:

Please amend paragraph [0015] on page 4 as follows:

D1

[0015]   In co-pending application 09/123,129; filed July 27, 1998; entitled *System and Method for Designing Integrated Circuits*, now U.S. Patent 6,269,277, the design techniques for designing for instance, an op-amp is described. In summary, this prior art computer aided design (CAD) system provides for the design and in optimizing of integrating circuits. It results in the automated synthesis of globally optimal circuit designs for a give circuit topology resulting directly from a user defined specification. Generally, the CAD system includes a library of integrated circuit topologies. The performance specifications for the integrated circuit topologies are described as posynomial functions of the design parameters. The performance specifications are combined with user defined design objectives and constraints to form a geometric program. One embodiment reformulates geometric programs as convex optimization problems, i.e. the problem of minimizing a convex function subject to convex inequalities constraints and linear equality constraints. This facilitates globally and efficiently solving geometric programs. New variables y_i = log x_i are defined, the logarithm of a polynomial f is taken to get

$$h(y)=\log(f(e^{y_1},...,e^{y_n}))=\log\left(\sum_k e^{a_k^T y + b_k}\right)$$

$$h(y) = \log\left(f\left(e^{y_1},....e^{y_n}\right)\right) = \log\left(\sum_k e^{a_k^T y + b_k}\right)$$